

## メモ

- Leveraging Hamiltonian Simulation Techniques to Compile Operations on Bosonic Devices  
<https://arxiv.org/abs/2303.15542>
- ZCU104 <https://docs.amd.com/v/u/en-US/ug1267-zcu104-eval-bd>
- Doubly-polylog-time-overhead fault-tolerant quantum computation

by a polylog-time parallel minimum-weight perfect matching decoder <https://arxiv.org/pdf/2503.13601>

- Circuit Quantum Electrodynamics <https://arxiv.org/abs/2005.12667>
- TQSim <https://github.com/meng-ubc/TQSim>
- Ozaki Scheme II: A GEMM-oriented emulation of floating-point matrix multiplication using an integer modular technique <https://arxiv.org/abs/2504.08009>