

論文読み

Suppressing quantum errors by scaling a surface code logical qubit

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 - we implement single-qubit rotations, controlled-Z (CZ) gates, reset and measurement, demonstrating similar or improved simultaneous performance
 - We attribute this rise to data qubits leaking into non-computational excited states and anticipate that the inclusion of leakage-removal techniques on data qubits would help to mitigate this rise
 - we compute an appropriately normalized correlation p_{ij} between detection events occurring on any two detectors i and j
 - We use a generalization of p_{ij} to determine these probabilities
 - To understand the contributions of individual components to our logical error performance, we follow
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