

## 一言メモ

- An FPGA-based 7-ENOB 600 MSample/s ADC without any External Components - [https://github.com/LukiLeu/FPGA\\_ADC](https://github.com/LukiLeu/FPGA_ADC)
- FPGA2015 ではこういうのもあったな . "200 MS/s ADC implemented in a FPGA employing TDCs" - <https://cas.tudelft.nl/pubs/Homulle15fpga.pdf>