

## ZYNQ MPSoC 関連データシート

大元 - <https://www.xilinx.com/products/silicon-devices/soc/zynq-ultrascale-mpsoc.html#documentation>

- UltraScale Architecture and Product Data Sheet: Overview - [https://www.xilinx.com/support/documentation/data\\_sheets/ds890-ultrascale-overview.pdf](https://www.xilinx.com/support/documentation/data_sheets/ds890-ultrascale-overview.pdf)
- Zynq UltraScale+ MPSoC Data Sheet: Overview - [https://www.xilinx.com/support/documentation/data\\_sheets/ds891-zynq-ultrascale-plus-overview.pdf](https://www.xilinx.com/support/documentation/data_sheets/ds891-zynq-ultrascale-plus-overview.pdf)
- Zynq UltraScale+ Device TRD - [https://www.xilinx.com/support/documentation/user\\_guides/ug1085-zynq-ultrascale-trm.pdf](https://www.xilinx.com/support/documentation/user_guides/ug1085-zynq-ultrascale-trm.pdf)
- Zynq UltraScale+ Device Packaging and Pinouts - [https://www.xilinx.com/support/documentation/user\\_guides/ug1075-zynq-ultrascale-pkg-pinout.pdf](https://www.xilinx.com/support/documentation/user_guides/ug1075-zynq-ultrascale-pkg-pinout.pdf)