

## 一言メモ

- HeteroCL: A Multi-Paradigm Programming Infrastructure for Software-Defined Reconfigurable Computing <https://vast.cs.ucla.edu/~chiyuze/pub/fpga19-heterocl.pdf>
- Restructuring Software Code for High-Level Synthesis Using a Graph-based Approach Targeting FPGAs - <https://repositorio-aberto.up.pt/bitstream/10216/115047/2/280925.pdf>
- Templates and Patterns: Augmenting High-Level Synthesis for Domain-Specific Computing - <http://kastner.ucsd.edu/wp-content/uploads/2013/08/admin/phd-thesis-matai.pdf>
- Parallel Programming for FPGAs - <http://kastner.ucsd.edu/wp-content/uploads/2018/03/admin/pp4fpgas.pdf>
- The Tides of EDA - <https://ptolemy.berkeley.edu/projects/embedded/research/hsc/class/papers/d6sang.lo.pdf>