

## Heart (3)

Keynote(3) FPGA accelerated HPC for Experimental Physics

実験物理の人があれこれ手を尽くす感じには、いつもながら感心させられる。

### 一言メモ

- A GUIDE FOR SESSION CHAIRS <https://emeryblogger.com/2016/06/08/a-guide-for-session-chairs/>
- Chrome 拡張の作り方 ( 超概要 ) <https://qiita.com/RyBB/items/32b2a7b879f21b3edefc>
- FPGA-Based Dynamically Reconfigurable SQL Query Processing <https://dl.acm.org/citation.cfm?id=2845087>
- Latency Numbers Every Programmer Should Know <https://gist.github.com/jboner/2841832>
- Writing OpenCL(TM) for FPGAs <https://www.iwocl.org/wp-content/uploads/iwocl-2019-tutorial-karl-qi-optimizing-opencl-for-intel-fpgas.pdf> - <<< ReadItLater
- Developer preview of TensorFlow Lite <https://hn.svelte.dev/item/15699084> - <<< ReadItLater
- ARM-software/ML-examples - <https://github.com/ARM-software/ML-examples> - <<< ReadItLater
- Ultra-low power development board for machine learning uses GoogleTensorFlow Lite - <https://www.microcontrollertips.com/ultra-low-power-development-board-ml-googletensorflow-lite/> - <<< ReadItLater
- SparkFun Edge [https://github.com/sparkfun/SparkFun\\_Edge](https://github.com/sparkfun/SparkFun_Edge) - <<< ReadItLater
- ZUCL: A ZYNQ UltraScale+ Framework for OpenCL HLS Applications <https://www.researchgate.net/publication/327368678>  
[ZUCL A ZYNQ UltraScale Framework for OpenCL HLS Applications](https://www.researchgate.net/publication/327368678) - <<< ReadItLater
- Recent advances in convolutional neural network acceleration <http://www.cse.cuhk.edu.hk/~byu/papers/J36-DNN-overview.pdf> - <<< ReadItLater