

一言メモ

- SoC-FPGA Design Guide 200.38.pdf - http://moodle.epfl.ch/pluginfile.php/1591974/mod_resource/content/4/SoC-FPGA%20Design%20Guide%20200.38.pdf (Mon Mar 23 18:42:24 2015 +0900)
- Lynx-EyED の電音鍵盤 新館 アルテラ CPLD で 150LE 未満のワンチップ CPU を作ったお話 - <http://lynxeyed.hatenablog.com/entry/2014/07/11/233018> (Mon Mar 23 18:36:41 2015 +0900)
- DE1-SoC で ROS とのこと - <http://qiita.com/kazuyamashi/items/a99e216b780ad4786a99> (Mon Mar 23 18:36:04 2015 +0900)
- IBM の Coherent Accelerator Processor Interface (CAPI) <http://www.nallatech.com/solutions/openpower-capi-developer-kit-for-power-8/> (Mon Mar 23 18:35:10 2015 +0900)
- TUTORIAL: ALTERA CYCLONE5 SOC BAREMETAL FROM SCRATCH - <https://samskalicky.wordpress.com/2014/06/06/tutorial-altera-cyclone5-soc-baremetal-from-scratch/> (Mon Mar 23 18:34:23 2015 +0900)
- SoCrates Workshop Presentation - http://ebv.jack-mrosek.de/s7HKwsmx37i/SoC_Workshop_HW/Files/SoCrates_Workshop_Presentation.pdf (Mon Mar 23 18:33:45 2015 +0900)
- SeriouslyEmbedded Control 7-Segment Display from HPS on DE1-SoC - <http://seriouslyembedded.com/7-segment/> (Mon Mar 23 18:29:35 2015 +0900)
- 電子工作の部屋 DE1-SoC に LCD(800x480) をつなげて X11 を動かしてみた - <http://debuota23.blog106.fc2.com/blog-entry-27.html> (Mon Mar 23 18:29:10 2015 +0900)
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- Altera 用 DS-5 のライセンス <http://ds.arm.com/ja/altera/altera-community-edition/>